

**IDS - 08/20/2004 ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	<b>SYSTEM AND METHOD FOR ARBITRATION BETWEEN SHARED PERIPHERAL CORE DEVICES IN SYSTEM ON CHIP ARCHITECTURES</b>
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Application Number : 10/711,084  
Confirmation Number: 5083  
First Named Applicant: Serafino Bueti  
Attorney Docket Number: BUR920040148US1  
Art Unit: 2112  
Examiner: Christopher E. Lee  
Search string: ( 6262594 or 6496880 or 6587905 or 6587932 or 6636949 or 20020091826 or 20030018738 or 20030051103 or 20030202530 ).pn

**US Patent Documents****Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
CEL	1	6262594	2001-07-17	Cheung et al.		326	38
	2	6496880	2002-12-17	Ma et al.		710	38
	3	6587905	2003-06-01	Correale, Jr. et al.		310	107
	4	6587932	2003-07-01	Tournier		711	151
CEL	5	6636949	2003-10-21	Barroso et al.		711	141

**US Published Applications****Note: Applicant is not required to submit a paper copy of cited US Published Applications**

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
CEL	1	20020091826	2002-07-11	Comeau et al.		709	226
	2	20030018738	2003-01-23	Boylan et al.		709	213
	3	20030051103	2003-03-13	Larnder et al.		711	147
CEL	4	20030202530	2003-10-30	Jenkins et al.		370	447

**Signature**

Examiner Name	Date
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/Christopher E. Lee/

06/09/2006



GROUP	2112
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[illegible][illegible]

CEL			A.C. Verschueren, M. Verhappen, B.D. Theelen and M.P.J. Stevens, "Arbitration in a Multi-Processor to Multi-Coprocessor Connection Switch;" ProRISCL/IEEE 10th Annual Workshop on Circuits and Systems and Signal Processing; pages 563 - 7 1999.

**DATE CONSIDERED** 06/09/2006

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.